



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Thomas W. Williams et al.

Assignee: Synopsys, Inc.

Title: INTELLIGENT TEST VECTOR FORMATTING TO REDUCE TEST VECTOR SIZE AND ALLOW ENCRYPTION THEREOF FOR INTEGRATED CIRCUIT TESTING

Serial No.: 09/728,022 File Date: November 30, 2000

Examiner: John J. Tabone Jr. Art Unit: 2133

Docket No.: SYN-0174

Date: April 20, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Initial Comments

The Office Action indicates that the signature of inventor Tom Williams is missing from the Declaration and Power of Attorney recorded on November 30, 2000. Figure 2 is objected to. The abstract is objected because of its length.

Claim 7 is objected to. Claims 10 and 17 are rejected under 35 U.S.C. 112, second paragraph.

Claims 1-3, 6-10, 13-17, and 19-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 5,444,716 (Jarwala). Claims 4-5, 11-12, and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Jarwala in view of U.S. Patent 6,101,622 (Lesmeister).

Claims 1-6 and 14-16 are cancelled, thereby leaving pending Claims 7-13 and 17-20. Claims 7, 10, and 17-20 are amended herein.